PATENT ABSTRACTS OF JAPAN

(11)Publication number: 03-066228(43)Date of publication of application: 20.03.1991

(51)Int.Cl. H03M 7/40

H04N 7/133

(21)Application number: 01-203285 (71)Applicant: MATSUSHITA ELECTRIC IND CO LTD

(22)Date of filing: **04.08.1989** (72)Inventor: **SHIGESATO TATSURO**

(54) BLOCK ENCODER AND DECODER

(57)Abstract:

PURPOSE: To minimize the deterioration in picture quality resulting from unable transmission of the last data by lending sequentially low frequency components important for visual sense at first so as to make the data rate constant.

CONSTITUTION: An inputted sample is subject to block processing into small blocks for the unit of executing DCT(discrete cosine transformation) at a small block forming processing means 1 at first end rearranged orthogonal components are quantized by a quantization means 5. A quantized value obtained by the quantization means 5 is subject to variable length coding by a variable length coding means 6 and rearranged in the unit of a large block by a transmission means 7 and a lower frequency component is sent sequentially. When the data quantity after coding is too large and all code words cannot be sent, code words for



higher frequencies whose transmission order is later cannot be sent, but since distortion of high frequency components is not eminent in general in a visual sense in comparison with the distortion of lower frequency components, the deterioration in the picture quality is minimized.